

Notice of References Cited	Application/Control No. 09/866,874	Applicant(s)/Patent Under Reexamination LIM, YOUNG-WON	
	Examiner Andrew L Nalven	Art Unit 2134	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,347,143	02-2002	Goff et al.	380/37
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	J	US-			
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FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hardware Design and Performance Estimation of□□The 128-bit Block Cipher CRYPTON□□□□Eunjong Hong, Jai-Hoon Chung, and Chae Hoon Lim□□
	V	Efficient 8-Cycle DES Implementation□□Young Won Lim
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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